

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Watts

Serial No.: 08/568,904

Filed: 12/07/95

For:

TI-20567

Examiner: R. Dharia

Art Unit: 2781

AMENDMENT TRANSMITTAL FORM

REAL-TIME THERMAL MANAGEMENT FOR COMPUTERS

Assistant Commissioner for Patents

Washington, D. C. 20231

Dear Sir:

MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(A)

I hereby certify that the above correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231 on July 28, 1998.

Transmitted herewith is an Amendment in the above-identified application. The fee has been calculated as shown below:

CLAIMS AS AMENDED						
	CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NUMBER PREVIOUSLY PAID FOR	PRESENT EXTRA	RATE	ADDITIONAL FEE
Total Claims	45	Minus	50	= 0	x \$22 =	\$ 000.00
Independent Claims	6	Minus	6	= 0	x \$82 =	\$ 000.00
TOTAL ADDITIONAL FEE FOR THIS AMOUNT						\$ 000.00

Under 37 C.F.R. §1.15(k) charge the total additional fee, and any further fees, or credit overpayment to Deposit Account No. 20-0668. This form is submitted in triplicate.

Ronald O. Neerings, Reg. No. 34,227

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Watts

Serial No.: 08/568,904

Filed: 12/07/95

For:

REAL-TIME THERMAL MANAGEMENT FOR COMPUTERS

AMENDMENT – 37 C.F.R. § 115

Assistant Commissioner for Patents

Washington, D.C. 20231

Dear Sir:

EXPRESS MAIL Mailing Label Number EM575686507US I hereby certify that the above correspondence is being deposited with the U.S. Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated below and is addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231 on July 28,

TI-20567

Examiner: R. Dharia

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Responsive to the Office Action dated April 28, 1998, please amend the above-identified application as follows:

IN THE CLAIMS:

- 9. (amended) An apparatus, comprising:
- a provision for user input;
- a provision for output;
- a central processing unit (CPU) coupled to said user input and output;
- a monitor for monitoring temperature within said apparatus; and
- a clock manager adapted to receive a control signal from said monitor, said clock manager reducing central processing unit (CPU) clock speed when [said] a detected temperature level is at and above a selected reference temperature level and said CPU is not processing critical I/O.